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000513

7590

09/25/2006

WENDEROTH, LIND & PONACK, L.L.P.

2033 K STREET N.W.

SUITE 800

WASHINGTON, DC 20006-1022

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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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10/773,276

02/09/2004

Tomoo Kimura

2004_0156A

1091

TITLE OF INVENTION: LOGIC CIRCUIT OPTIMIZING METHOD, LOGIC CIRCUIT OPTIMIZING DEVICE AND LOGIC CIRCUIT COMPOSING DEVICE

APPLN. TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE FEE	TOTAL FEE(S) DUE	DATE DUE
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nonprovisional

NO

\$1400

\$300

\$0

\$1700

12/26/2006

EXAMINER	ART UNIT	CLASS-SUBCLASS
SIEK, VUTHE	2825	716-002000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

☐ Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.

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(1) the names of up to 3 registered patent attorneys or agents OR, alternatively,

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Wenderoth, Lind &
& Ponack, L.L.P.

2

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3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE

Matsushita Electric Industrial Co., Ltd.

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Osaka, Japan

00000113 10773276

01 FC:1501

1400.00 OP

02 FX:1504

300.00 OP

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☐ Corporation or other private group entity ☐ Government

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☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27.

☐ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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Authorized Signature

Kenneth W. Fields

Date October 19, 2006

Typed or printed name

Kenneth W. Fields

Registration No. 52,430

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